Serial Number: 09/945,394

Filing Date: August 30, 2001

Title: CIRCUIT BOARD PLANE INTERLEAVE APPARATUS AND METHOD

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REMARKS

Claim 65 is added. No claims are canceled. As a result claims 1-65 remain pending in the application. Claims 7, 12-18, 23, 41 and 49 stand withdrawn.

Election Requirement

In response to the Restriction Requirement mailed October 27, 2003, the Applicant provisionally elects, with traverse, claims 1-6, 8-11, 19-22, 24-40, and 42-48. The Restriction Requirement states that

In the instant case the product can be made by different process, for example, first forming a dielectric layer, forming first and second conductive layers having first and second interstices on top and bottom surfaces of the dielectric layer respectively, then engaging the first and second interstices.

Applicant respectfully asserts that this process is included in present method claim 50. It appears that the examiner is reading a required sequence into the method steps of claim 50 that is not claimed at this time. Accordingly, claim 50 is linked to the elected apparatus claims as set forth in MPEP 809.03.

Applicant herein adds a further linking claim pursuant to MPEP 809.03. New claim 65 links the elected apparatus claims and method claims 50-64. Consideration of all of the pending claims is requested.

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CIRCUIT BOARD PLANE INTERLEAVE APPARATUS AND METHOD

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CONCLUSION

The Examiner is invited to contact the Applicant's Representative Mark V. Muller at (210) 308-5677, or the undersigned, if there are any questions regarding this Response or if prosecution of this Application may be assisted thereby. If necessary, please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

Respectfully submitted,

STEVE VAN KIRK

By his Representatives,

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Date 25 May '03

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this **25** day of November, 2003.

Name